Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore (MP)
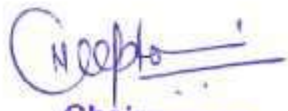
Choice Based Credit System (CBCS) Scheme

B.Tech in Electronics and Communication - IOT
VI - SEMESTER

Sr. No.	Subject Code	Name of Subject	Course Code	Teaching Scheme/ Week			Examination Scheme					Total Marks	Credits
							Theory			Practical			
				L	T	P	End Sem Unversity Exam	Two Term Exam	Teachers Asses- ment*	End Sem Unversity Exam	Teachers Asses- ment*		
1	BTCSIOT601	Web Programming		3	1	2	60	20	20	30	20	150	5
2	BTEC603	Discrete Time Signal Processing		3	1	2	60	20	20	30	20	150	5
3	BTECIOT601	Data Communication and Computer Networks		3	1	2	60	20	20	30	20	150	5
4	BTECIOT602	Wireless and Mobile Communication		3	1	0	60	20	20	0	0	100	4
5	BTEC525/ BTEI604/ BTEC714	Elective: 1 FPGA based System Design 2 Digital Image Processing 3 Embedded Systems for Robotics		3	1	2	60	20	20	30	20	150	5
6	BTEC606	Technical Communication and Soft Skills		1	0	0	0	0	50	0	0	50	1
7	BTEC605	Personality Development-III		0	0	2	0	0	0	0	50	50	1
		TOTAL		16	5	10	300	100	100	120	180	800	26

*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/ **/Participation** in Class, given that no component shall exceed more than 10 marks.

Chairperson
Board of Studies
Shri Vaishnav Vidyapeeth Vishwavidyalaya
Indore

Registrar
Shri Vaishnav Vidyapeeth Vishwavidyalaya
INDORE (M.P.)